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9/24/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

In Re: SARTSCHEV, Ronald A., et al.

Serial No: 10/015865

Filed: 12/12/2001

For: COMPACT ATE WITH TIME
STAMP SYSTEM

Group: 2133

Examiner:

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Technology Center 2100

April 17, 2003

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INFORMATION DISCLOSURE STATEMENT

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In accordance with the duty of candor, the Applicant wishes to make of record the following documents listed on the attached form.

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1. Within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d);
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3. Before the mailing of a first Office action on the merits; or
4. Before the mailing of a first Office action after the filing of a request for continued examination under § 1.114,

and therefore no additional fees are due,

OR

☐ B) This statement is being filed—

1. Before the mailing date of any of a final action under § 1.113, a notice of allowance under § 1.311, or an action that otherwise closes prosecution in the application; or
2. On or before payment of the issue fee,

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☐ A fee set forth in § 1.17(p). The Commissioner is hereby authorized to charge \$180, the payment of fees under § 1.17(p), to Deposit Account Number 20-0515; or

☐ A statement specified in 37 C.F.R. 1.97 (e):

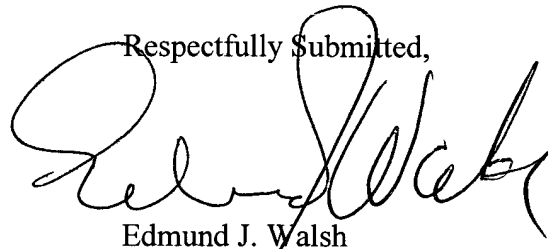
☐ Each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement; or

☐ No item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information

disclosure statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of the information disclosure statement.

This information disclosure statement is not to be construed as a representation that a search has been made, nor is it to be construed as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in § 1.56(b).

Respectfully Submitted,



Edmund J. Walsh

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INFORMATION DISCLOSURE STATEMENT

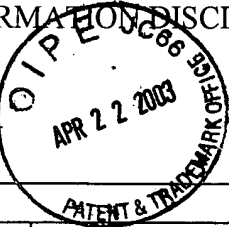
Atty. Docket No. 1490

Serial No. 10/015865

Applicant: SARTSCHEV,
Ronald A., et al.

Filing Date: 12/12/2001

Group: 2133



U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if appropriate
AA	6285963	Sep 4, 2001	West	702	117	Nov 30, 1999
AB						
AC						
AD						
AE						
AF						

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FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
AG							
AH							
AI							

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

AJ	MOTA, M. et al.: "A flexible multi-channel high-resolution time-to-digital converter ASIC", 2000 IEEE Nuclear Science Symposium. Conference Record (Cat. No. OCH37149), 2000 IEEE Nuclear Science Symposium. Conference Record, Lyon, France, 15-20 Oct. 2000, vol.2, pages 9/155-9 189, XP002236607 2000, Piscataway, NJ, USA, IEEE, USA ISBN: 0-7803-6503-8, page 9-155, page 9-158
AK	MOTA, M. et al: "A four-channel self-calibrating high-resolution time to digital converter" Electronics, Circuits and Systems, 1998, IEEE International Conference on Lisboa, Portugal 7-10 Sept. 1998, Piscataway, NJ, USA, IEEE, US, 7 Sept 1998 (1998-09-07), pages 409-412, XP010366204, ISBN: 0-7803-5008-1, page 409, page 410
AL	CHRISTIANSEN J.: "An integrated high resolution CMOS timing generator based on an array of delay locked loops", IEEE Journal of Solid State Circuits, IEEE Inc. New York, US, vol. 31, no. 7, 1 July 1996 (1996-07-01), pages 952-957, XP000632381, ISSN: 0018-9200, the whole document
AM	
AN	
AO	

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